

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10030687 on April 12, 2002

- 10/630,687
- 5 326/98 (2 OR, 3 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/93 CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR GATES
 - 326/95 ..Field-effect transistor
 - 326/98 ..MOSFET
 - 3 326/126 (2 OR, 1 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/104 FUNCTION OF AND, OR, NAND, NOR, or NOT
 - 326/124 ..Bipolar transistor (e.g., RTL, DCTL, etc.)
 - 326/126 ..Emitter-coupled or emitter-follower logic
 - 3 326/17 (0 OR, 3 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/17 ACCELERATING SWITCHING
 - 3 326/68 (1 OR, 2 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)
 - 326/63 ..Logic level shifting (i.e., interface between devices of different logic families)
 - 326/68 ..Field-effect transistor (e.g., JFET, MOSFET, etc.)
 - 3 326/83 (0 OR, 3 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)
 - 326/82 ..Current driving (e.g., fan in/out, off chip driving, etc.)
 - 326/83 ..Field-effect transistor
 - 3 331/57 (1 OR, 2 XR)
 - Class 331 : OSCILLATORS
 - 331/57 RING OSCILLATORS
 - 3 365/156 (0 OR, 3 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/129 SYSTEMS USING PARTICULAR ELEMENT
 - 365/154 ..Flip-flop (electrical)
 - 365/156 ..Complementary
 - 3 365/189.01 (0 OR, 3 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 3 365/203 (2 OR, 1 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/203 ..Precharge
 - 3 365/207 (2 OR, 1 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/206 ..Noise suppression

- 365/207 ..Differential sensing
- 3 365/227 (1 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/226 POWERING
 - 365/227 ..Conservation of power
- 3 365/233 (1 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/230.01 ADDRESSING
 - 365/233 ..Sync/clocking
- 2 326/110 (2 OR, 0 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/104 FUNCTION OF AND, OR, NAND, NOR, or NOT
 - 326/109 ..Bipolar and FET
 - 326/110 ..Bi-CMOS
- 2 326/121 (1 OR, 1 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/104 FUNCTION OF AND, OR, NAND, NOR, or NOT
 - 326/112 ..Field-effect transistor (e.g., JFET, etc.)
 - 326/119 ..MOSFET (i.e., metal-oxide semiconductor field-effect transistor)
 - 326/121 ...CMOS
- 2 326/18 (0 OR, 2 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/17 ACCELERATING SWITCHING
 - 326/18 ..Bipolar transistor
- 2 326/24 (2 OR, 0 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
 - 326/22 ..Input noise margin enhancement
 - 326/23 ..With field effect-transistor
 - 326/24 ...Complementary FET's
- 2 326/29 (0 OR, 2 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
 - 326/29 ..Pulse shaping (e.g., squaring, etc.)
- 2 326/33 (0 OR, 2 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
 - 326/31 ..Signal level or switching threshold stabilization
 - 326/33 ..Bias or power supply level stabilization
- 2 326/97 (2 OR, 0 XR)
 - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 - 326/93 CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR GATES
 - 326/95 ..Field-effect transistor
 - 326/96 ..Two or more clocks (e.g., phase clocking, etc.)
 - 326/97 ...MOSFET
- 2 327/212 (0 OR, 2 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR

DEVICES, CIRCUITS, AND SYSTEMS

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING

327/185 .Particular stable state circuit (e.g.,
tristable, etc.)327/199 ..Circuit having only two stable states (i.e.,
bistable)

327/208 ...Including field-effect transistor

327/212With clock input

2 327/218 (0 OR, 2 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING

327/185 .Particular stable state circuit (e.g.,
tristable, etc.)327/199 ..Circuit having only two stable states (i.e.,
bistable)

327/215 ...Having at least two cross-coupling paths

327/218D type input

2 327/291 (0 OR, 2 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING

327/291 .Clock or pulse waveform generating

2 327/530 (2 OR, 0 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR
SYSTEM

327/530 .With specific source of supply or bias voltage

2 327/536 (1 OR, 1 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR
SYSTEM

327/530 .With specific source of supply or bias voltage

327/534 ..Having particular substrate biasing

327/535 ...Having stabilized bias or power supply level

327/536Charge pump details

2 327/541 (1 OR, 1 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR
SYSTEM

327/530 .With specific source of supply or bias voltage

327/538 ..Stabilized (e.g., compensated, regulated,
maintained, etc.)

327/540 ...With voltage source regulating

327/541With field-effect transistor

2 327/544 (0 OR, 2 XR)

Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS

- 327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR SYSTEM
- 327/530 .With specific source of supply or bias voltage
- 327/544 ..Power conservation or pulse type
- 2 331/66 (0 OR, 2 XR)
 - Class 331 : OSCILLATORS
 - 331/65 WITH DEVICE RESPONSIVE TO EXTERNAL PHYSICAL CONDITION
 - 331/66 .Temperature or light responsive
- 2 331/75 (0 OR, 2 XR)
 - Class 331 : OSCILLATORS
 - 331/74 COMBINED WITH PARTICULAR OUTPUT COUPLING NETWORK
 - 331/75 .Space discharge or unilaterally conductive device in output network
- 2 363/60 (1 OR, 1 XR)
 - Class 363 : ELECTRIC POWER CONVERSION SYSTEMS
 - 363/25With automatic control of the magnitude of output voltage or current
 - 363/59 .With voltage multiplication means (i.e., $V_{out} > V_{in}$)
 - 363/60 ..Including semiconductor means
- 2 365/189.05 (1 OR, 1 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/189.05 .Having particular data buffer or latch
- 2 365/204 (0 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/204 .Accelerating charge or discharge
- 2 365/206 (0 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/206 .Noise suppression
- 2 365/210 (1 OR, 1 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/206 .Noise suppression
 - 365/207 ..Differential sensing
 - 365/209 ...Magnetic
 - 365/210Reference or dummy element